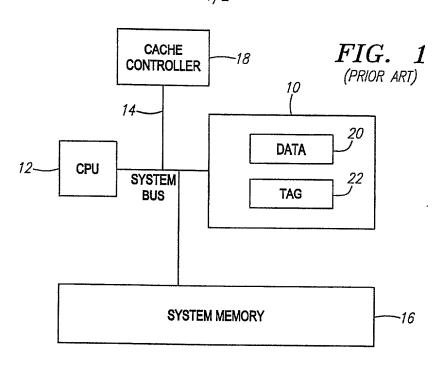
A Method For Recovering From Soft Errors in Microprocessor Cache Memories

TAYLOR and ALLEN

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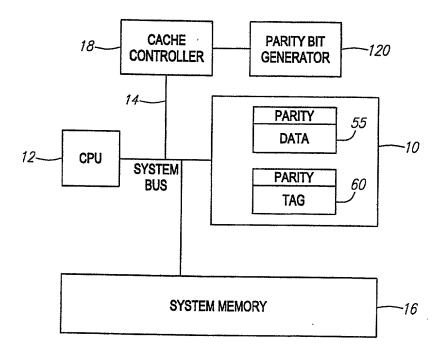


FIG. 2

Method For Recovering From Soft Errors in Microprocessor Cache
Memories

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